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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/612,383	07/01/2003	Ka Shing Kenny Kwan	P/4076-54	3682

2352 7590 06/24/2005

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EXAMINER

JOHNSON, JONATHAN J

ART UNIT	PAPER NUMBER
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1725

DATE MAILED: 06/24/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/612,383

Applicant(s)

KWAN ET AL.

Examiner

Jonathan Johnson

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 02 June 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-25 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-13 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☒ Claim(s) 1-25 are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-13 are rejected under 35 U.S.C. 102(b) as being anticipated by Hisataka (JP 01-296636). Hisataka teaches a bump forming device (figure 7, item 7); a chamber system adapted to house the semiconductor devices (Figure 1, item 10); a gas supply for supplying an inert gas into the chamber system (figure 1, item 31); and a support table for supporting the semiconductor devices during bumping, said support table being operative to move the semiconductor devices from a bumping site into the chamber system after bumping (figure 1, item 3); including an oxidation reduction device for introducing a supply of inert gas to the bumping site (figure 1, item 31); wherein the bumping site is adjacent to an opening of the chamber system (figure 3, item 17); including a shroud positioned around the bumping site for covering one or more bumped semiconductor devices (figure 1, item 15); wherein the shroud includes nozzles for introducing a supply of inert gas onto the semiconductor devices (figure 1, item 31); wherein the chamber system comprises an outer chamber, and an inner chamber that is houseable within the outer chamber (figure 6, items 11 and 2); wherein the inner chamber is removable from the outer chamber (figure 1, items 11 and 2); wherein the removable inner chamber comprises at least a portion of the support table and an inner chamber cover for forming an enclosure around bumped

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semiconductor devices (figure 1, item 11); including gas tubings that are detachably connectable to the inner chamber for bringing an inert gas into the inner chamber from an external source (figure 1, item 31); wherein the support table is coupled to a positioning device that is operative to move the support table along a first axis (figure 3, x axis); wherein the chamber system is coupled to a positioning device that is operative to move the chamber system along a second axis perpendicular to the first axis (figure 3, y axis); wherein the bump forming device is an ultrasonic wire bonder (abstract); wherein the semiconductor devices are comprised in a semiconductor wafer and the support table capable of supporting a wafer on the table (figure 1, item 3).

Conclusion

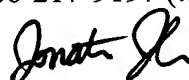
The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jonathan Johnson whose telephone number is 571-272-1177. The examiner can normally be reached on M-Th 7:30 AM-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Dunn can be reached on 571-272-1171. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Jonathan Johnson
Primary Examiner
Art Unit 1725